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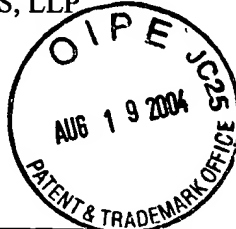
501.35437CV2

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20457 7590 05/19/2004

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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/416,959	10/13/1999	MAKOTO YOSHIDA	501.35437VX1	6819

TITLE OF INVENTION: SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND PROCESS FOR MANUFACTURING THE SAME

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	08/19/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
POMPEY, RON EVERETT	2812	257-323000

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(A) NAME OF ASSIGNEE

Hitachi, Ltd.

(B) RESIDENCE: (CITY AND STATE OR COUNTRY)

Tokyo, Japan

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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(Authorized Signature) Gregory E. Montone (Date) August 19, 2004

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